CIMaX™ SP2

DVB-CI and OpenCable™ Interface Controller

Dual slot CableCARD™, DVB Common Interface and OpenCable™ hardware controller for Digital TV receivers

The CIMaX™ SP2 is a CableCARD™ and DVB Common Interface Controller Chip capable of supporting one or two OpenCable™ or Common Interface slots of digital TV receiver devices or Digital Cable-Ready TV sets.

The CIMaX™ SP2 supports serial and/or parallel interface at tuner and MPEG-decoder level as well as standby mode for power consumption reduction.

Digital TV Solutions

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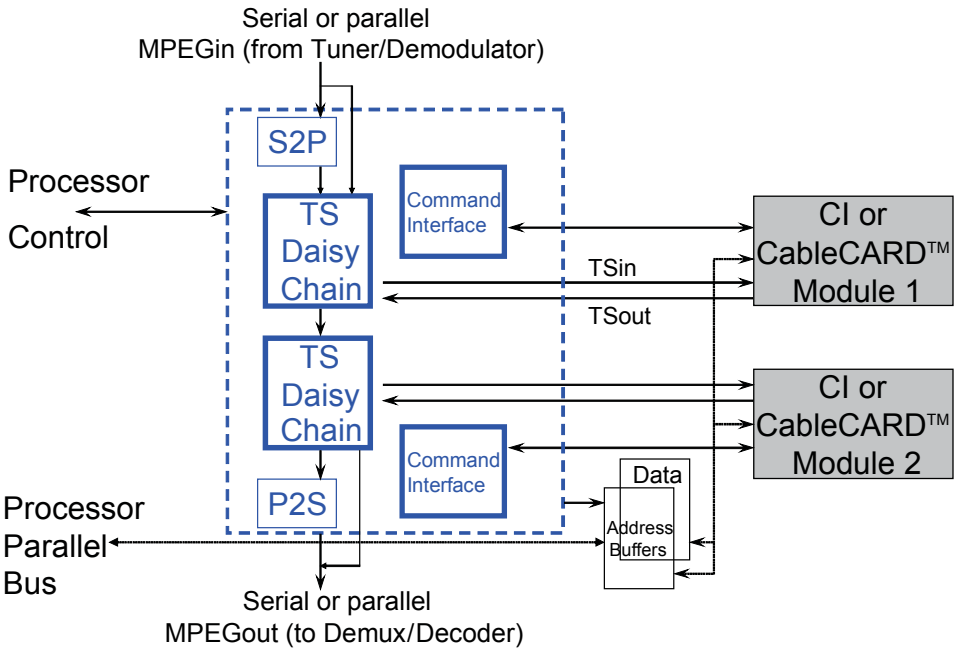
CIMaX™ SP2

Block Diagram of the CIMaX™ SP2



The CIMaX™ SP2 is an advanced development of the CIMaX™ 2.0. It is pin-to-pin as well as electrically compatible with the CIMaX™ 2.0. It is also firmware compatible. The CIMaX™ SP2 can be used in any CIMaX™ 2.0 design with no modification.
SCM Microsystems' CIMaX™ SP2 enables an optimised, homogeneous and complete solution to implement CableCARD™ interface for OpenCable™ and DVB Common Interface for DVB broadcasting. It generates the Data Channel / Command Interface signals from the local host processor bus and contains the Daisy Chaining of the Transport Stream.
SCM Microsystems has also a CableCARD™ solution with a CableCARD™ driver dedicated to the CIMaX™ SP2.

Features

- **Module Interface**
  - 2 full independent modules capability
  - Common Interface Standard compliant
    - DVB-CI (CENELEC EN-50221)
  - OpenCable™ specification compliant
    - SCTE 28 2003
    - DAVIC v1.2 (CA0 interface)
  - Memory PCMCIA compliance (R2)
    - 8-bit data access
    - 26-bit address Memory Card
  - Attribute Memory access (CIS, Tupple)
  - High speed capability
    - Up to 20Mbits/s on Command Interface
    - Up to 100Mbits/s on Transport Stream
  - Polling and Interrupt modes
  - Hot Insertion (Automatic and Reset VCC handling)
  - 3.3V or 5V I/O buffers
- **PQFP 128 package**

- **Host microprocessor Interface**
  - Universal Control Signal Generator (UCSG)
    - PC Card control signals generation
    - Supports PowerPC, ARM, ST20, 68xxx, TMS, LSI 64008, TC81220F, IDTR3041 host microprocessors
  - I2C port
    - CIMaX™ SP2 Set-up
    - Slot selection
    - Cascade mode management (up to 4 CIMaX™ SP2)
  - Chip Select bank and Interrupt facilities
  - 3.3V or 5V I/O buffers
- **Digital Video Stream Interface**
  - MPEG II Transport Stream compliant
  - Parallel or Serial Transport Stream
  - 3.3V or 5V I/O buffer for direct interface with FEC and DEMUX ICs
- **Standby mode**

Technical data are subject to change without notice.